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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

New York, New York

Toru ISHIKAWA

Date: June 28, 2004

Serial No.: 10/693,191

Group Art Unit: 2811

Filed: October 23, 2003

Examiner: Not Assigned

For: TIMING ADJUSTMENT CIRCUIT AND SEMICONDUCTOR
DEVICE INCLUDING THE SAME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUBMISSION

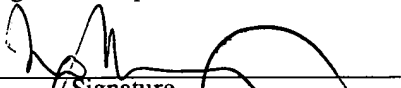
Sir:

Submitted herewith is a copy of art listed in or relevant to the specification
together with a form listing the same for the convenience of the Examiner.

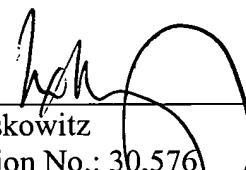
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Signature
June 28, 2004
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Respectfully submitted,


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Enclosures